Remarks/Arguments

Claims 5-24 are pending in the present application. Claims 5-24 have been presented herewith. Claims 1-4 have been canceled.

Also by this Preliminary Amendment, the specification has been revised to identify the parent application.

With regard to the drawings, Fig. 8 has been corrected as indicated in the red-inked Annotated Marked-up Drawings enclosed herewith, wherein MOS transistor 76 has been illustrated correctly as p-channel as supported on page 16, lines 1-3 of the application, and the depiction of n-channel MOS transistor 75 has been corrected. Also, Fig. 9 has been corrected wherein MOS transistor 85 has been illustrated correctly as p-channel as supported on page 18, lines 5-6 of the application, and the depiction of n-channel MOS transistor 86 has been corrected.

Since the corrections as depicted in the Annotated Marked-up Drawings are merely for the purpose of improving accuracy, the drawing corrections should not be construed as new matter.

The formal drawings as provided along with this application incorporate the drawing corrections therein.

Also, pages 15, 16 and 18 have been amended to accurately describe the sources and drains of MOS transistors 75 and 85.

Favorable consideration and early allowance of the present application are earnestly solicited.

Preliminary Amendment dated March 31, 2004

In the event that there are any outstanding matters remaining in the present application, please contact Andrew J. Telesz, Jr. (Reg. No. 33,581) at (703) 715-0870 in the Washington, D.C. area, to discuss these matters.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment for any additional fees that may be required, or credit any overpayment, to Deposit Account No. 50-0238.

Respectfully submitted,

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Enclosures: Two (2) sheets of Annotated Marked-up Drawings

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